



राष्ट्रीय प्रौद्योगिकी की संस्थान सिलचर  
National Institute of Technology Silchar  
(राष्ट्रीय महत्व का संस्थान)

(An Institute of National Importance)

www.nits.ac.in, Phone: 03842-242914, Fax: 03842-224797, Assam-788010 (INDIA)

Advertisement No: NITS/Dean (RC)/ECE/640/05/2025

Date: 24.07.2025

Applications are invited from Indian citizens for a temporary post of **Project Associate-I(PA-I) and Project Associate-II(PA-II)** in the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

|                                     |                                                                                             |
|-------------------------------------|---------------------------------------------------------------------------------------------|
| Title of the project                | <b>Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.</b> |
| Sponsored Agency                    | Ministry of electronics & Information Technology (MeitY), Government of India               |
| Project sanction number             | EE-9/2/2021-R&D-E Dated 22 May2023                                                          |
| No. of posts                        | <b>One ,Project Associate-I and one Project Associate-II</b>                                |
| Duration                            | 1years and extendable till the completion of the project (10/02/2027)                       |
| Principal Investigator (P.I& Co-PI) | <b>Prof. K.L. Baishnab &amp;Dr. Koushik Guha</b>                                            |

**General Information and Terms &Conditions**

|   |                                                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
|---|---------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | Essential Qualification and experiences           | <b>For PA-I</b><br>BE/B.Tech with valid GATE Score or M.E/M.Tech in Microelectronics & VLSID esign with good academic record from are cognized University/Institute.<br><b>For PA-II</b><br>M.E/M.Tech in Microelectronics & VLSI Design with minimum 5 Years experience in VLSI Circuit Design and Chip Tape-out with SCL180. PhD in relevant field is preferred.                                                                                                                                                         |
| 2 | Desirable Experience for both the posts           | <b>PA-I</b><br>Knowledge of Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation, Knowledge of bio-logical background related to the project work.<br><b>PA-II</b><br>Knowledge of working in Linux environment, installation of EDA tools and server maintenance, Chip Tape out, Analog Circuit Design, Analog Layout, Analog Mixed Signal Design                                                                                                                                                         |
| 3 | Salary                                            | <b>For PA-I</b> <ul style="list-style-type: none"><li>Rs.31000/-per month with valid Gate score and Rs25000/- per month without valid Gate score for M. Tech candidates. +HRA as per institute Norms.</li></ul> Rs.25000/-per month with valid Gate score for B. Tech candidates.+ HRA as per institute Norms.<br><b>For PA-II</b> <ul style="list-style-type: none"><li>Rs. 60,000/- per month + HRA as per institute norms</li></ul> Candidates may also get an opportunity to pursue M.Tech/PhD as per Institute Norms. |
| 4 | Last date of receiving filled up application form | <b>08<sup>th</sup> August,2025</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| 5 | Reporting                                         | Applicants are requested to submit the signed scanned copy of their filled- in application form a long with CV and the all supporting documents to the P.I. through email at <a href="mailto:klb@ece.nits.ac.in">klb@ece.nits.ac.in</a> and <a href="mailto:koushik@ece.nits.ac.in">koushik@ece.nits.ac.in</a> with subject line“ Application for the post of <b>Project Associate-I/Project Associate-II</b> under MeitY projectEE-9/2/2021-R&D-E”.Call letter for short listed candidates will be sent through email.    |
| 6 | Date of interview, time, And venue                | <b>Interview date and mode (online/offline) will be notified in due course of time through email.</b>                                                                                                                                                                                                                                                                                                                                                                                                                      |
| 7 | No objection certificate                          | Employed candidates are to submit no objection certificate from his/her Employer at the time of interview.                                                                                                                                                                                                                                                                                                                                                                                                                 |
| 8 | Institute right                                   | The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruite difsuitable candidates are not found.                                                                                                                                                                                                                                                                                                                         |
| 9 | Signing of the agreement                          | Selected candidates shall have to sign an agreement at the time of joining.                                                                                                                                                                                                                                                                                                                                                                                                                                                |

Sd/-

Associate Dean(R&C)

NIT Silchar.

National Institute of Technology Silchar

FORMAT of APPLICATION

for \_\_\_\_\_

Paste recent  
passport size  
colored  
photograph

1. Advertisement no. .... dated:
2. Title of the Project- .....  
.....
3. Department:
4. Name of the applicant (in block letters):
5. Father's/Husband's Name:
6. Date of Birth (DD-MM-YYYY):
7. Age as on ..... :
8. Postal Address:
  
- E-mail:                      Phone no.:
9. Permanent Address:
  
10. Nationality:
11. Marital Status:
12. Whether belongs to SC/ST/PH/OBC: (If yes, attach certificate):
13. Educational Qualification: (Starting from Class X)

| Exam. passed | Board/ University | Division | % age of marks/ CPI | Year of passing | Subjects |
|--------------|-------------------|----------|---------------------|-----------------|----------|
|              |                   |          |                     |                 |          |
|              |                   |          |                     |                 |          |
|              |                   |          |                     |                 |          |
|              |                   |          |                     |                 |          |
|              |                   |          |                     |                 |          |

14. NET/GATE Examinations Passed:

| I. NET  |                 |            | II. GATE |       |            |
|---------|-----------------|------------|----------|-------|------------|
| Subject | Qualifying date | Valid upto | Subject  | Score | Valid upto |
|         |                 |            |          |       |            |

15. Details of employments:

| Sl. No. | Name of the Organization | Name of the post | Period | Scale of pay and present pay | Nature of duty |
|---------|--------------------------|------------------|--------|------------------------------|----------------|
|         |                          |                  |        |                              |                |
|         |                          |                  |        |                              |                |
|         |                          |                  |        |                              |                |

16. Any other relevant information that you may like to furnish:

**Declaration**

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:

Place

Signature of the applicant